

09/06/00

Class	Subclass
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100	100.1

FIGURE CLASSIFICATION

Best Available Copy

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED

APPLICATION NO.
09/656541

CONT/PRIOR

CLASS
703

SUBCLASS
31

ART UNIT	
2123	

EXAMINER SHARON
Travis

APPLICANTS

William Beausoleil
R. Cook
Tak-kwong Ng
Helmut Roth

TITLE

High speed software driven emulator comprised of a plurality of emulation processors with a method to allow high speed bulk read/write operation synchronous DRAM while refreshing the memory.

ISSUING CLASSIFICATION

[illegible]

**TERMINAL
DISCLAIMER**

DRAWINGS

Sheets Drwg.

Figs. Drwg.

Print Fig.

CLAIMS ALLOWED

Total Claims

Print Claim for O.G.

☐ The term of this patent subsequent to _____ (date) has been disclaimed.

☒ The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. 6,618,698

NOTICE OF ALLOWANCE MAILED

ISSUE FEE

Amount Due

Date Paid

☐ The terminal ____ months of this patent have been disclaimed.

(Legal Instruments Examiner)

(Date)

ISSUE BATCH NUMBER

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(FACE)